

PATENT ABSTRACTS OF JAPAN

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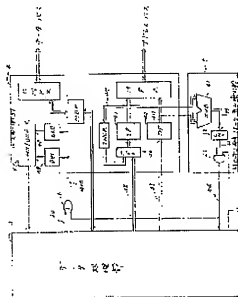
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(21)Application number : 58-161417 (71)Applicant : NEC CORP
(22)Date of filing : 02.09.1983 (72)Inventor : KIMURA SHINYA

(54) INFORMATION PROCESSING DEVICE

(57)Abstract:

PURPOSE: To emulate programs of a computer which has not the instruction prefetch function, by comparing the difference between instruction and data pointers with a fixed value to detect the necessity of refetching of a self-correction instruction and fetching again the instruction. CONSTITUTION: The address where data should be written is written in a data pointer DP42 by a data processing part 3. If the difference between the DP42 and an instruction pointer IP41 is ≥ 1 and is smaller than a maximum instruction storage capacity of an instruction buffer INSTBF47, a self-correction fetch completion SMIPF signal 55 is outputted. When a data write request 31 is outputted from the data processing part 3, an initializing signal 56 is supplied, and queue read and write counters 48 and 49 are initialized, and the INSTBF47 becomes empty. Next, a memory write signal is supplied. The address of an instruction to be next executed is transferred from the processing part 3 to the IP41 to complete preparations for instruction prefetch.



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